

CASE STUDY FOR UQC143H3 - VHDL for RTS

Attached is an initial Specification and Requirements Document for a car park barrier control system . This document forms the case study for Section One (1) of your exam. In preparation for this you should study the document and, in particular, consider the following issues.

- 1) Approaches to state machine design, especially with regard to VHDL and with regard to the case study. This should include ASM models.
- 2) Considerations of hardware requirements with respect to a state-machine and with respect to the limitations of the specification document. You should document any assumptions that you make.
- 3) Testing strategies for simulation purposes. If possible your testing strategies should take into consideration any constraints expressed in the document regarding the behaviour of the car park system.
- 4) You should also consider the fact that this design is for a modular system.

It is assumed that VHDL designs, testing strategies and simulation will be based on the use of the Alliance toolkits and simulator (asimut). You will be expected to make appropriate use of both behavioural and structural VHDL models as necessary.

You will be permitted to take up to 6 single-sided ,or 3 double-sided, pages of A4 notes and diagrams relating to this case study into the exam with you. These notes may be handwritten or computer generated (point size ≥ 10). You will be allowed to consult these note during the exam. These notes must be handed in with your exam scripts.

You are permitted to refer to diagrams and code fragments within your notes in your exam answers provided that the references are unambiguous and clearly identifiable.

Your notes should clearly indicate the source and/or authorship of any sections which are not your own.

Your notes must **CLEARLY** relate to the case study.

***** **Ensure that your student number is written on each sheet of notes** *****